

IN THE CLAIMS

Please cancel claims 53, 60, 74 and 83.

Please amend the following claims:

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5. (Amended) The display apparatus according to claim 1, wherein each capacitance element of the pixel is set so a second capacitance ratio  $\alpha_{st}$  shown by (Expression 57) is substantially constant.

(Expression 57)

$$\alpha_{st} = C_{st} / C_{tot}$$

6. (Amended) The display apparatus according to claim 1, wherein each capacitance element of the pixel is set so a the second capacitance ratio  $\alpha_{st}$  shown by (Expression 58) increases continuously or in stages according to the distance from the power feeding edge of the scanning electrode.

(Expression 58)

$$\alpha_{st} = C_{st} / C_{tot}$$

7. (Amended) The display apparatus according to claim 1, wherein the display medium is a liquid crystal.

8. (Amended) The display apparatus according to claim 1, further comprising a means for overlapping a voltage to the driving circuit of the scanning signal via the storage capacitance.

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19. (Amended) The display apparatus according to claim 11, wherein each capacitance element of the pixel is set as the fourth capacitance ratio  $\alpha_{st1}$  shown by (Expression 60) is substantially constant.

(Expression 60)

$$\alpha_{st1} = C_{st1} / C_{tot}$$

20. (Amended) The display apparatus according to claim 11, wherein each capacitance element of the pixel is set so the fourth capacitance ratio  $\alpha_{st1}$  shown by (Expression 61) increases continuously or in stages according to the distance from the power feeding edge of the scanning electrode.

(Expression 61)

$$\alpha_{st1} = C_{st1} / C_{tot}$$

21. (Amended) The display apparatus according to claim 11, wherein a parallel monotonic capacitance is not formed between the pixel electrode and the opposite electrode via the display medium.

27. (Amended) The display apparatus according to claim 11, further comprising a means for overlapping a voltage to the driving circuit of the scanning signal via the storage capacitance.

56. (Amended) The display apparatus according to claim 54, wherein the gate pulse is applied to more than two gate wirings at the same time.